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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Serial No. .... 10/665,092  
Filing Date ..... September 18, 2003  
Inventor ..... Visweswariah  
Assignee ..... IBM Corporation  
Group Art Unit ..... 2825  
Examiner ..... Leigh Garbowski  
Attorneys Docket No. .... YOR920030401US1  
Title: System and Method for Incremental Statistical Analysis of Digital Circuits

**RESPONSE TO JANUARY 26, 2006 OFFICE ACTION**

To: Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

From: Satheesh K. Karra  
IBM Corporation  
Intellectual Property Law Dept.  
T. J. Watson Research Center  
1101 Kitchawan Road  
Yorktown Heights, NY 10598

In response to the January 26, 2006 Office Action, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Claim Amendments begin on page 4 of this paper.

Remarks begin on page 12 of this paper.

INTELLECTUAL PROPERTY LAW DEPARTMENT  
Thomas J. Watson Research Center  
1101 Kitchawan Road / Route 134, P.O. Box 218  
Yorktown Heights, New York 10598  
Fax: (914) 945-3281 - T/L 862-3281

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**FACSIMILE**

DATE: May 26, 2006

NO. OF PAGES TO FOLLOW: 21

TO: **Commissioner for Patents**

NAME:

ADDRESS:

PHONE NO.

FAX NO.

(571) 273-8300

FROM:

NAME:

Doris Avendano

PHONE NO.

914-945-3279

LOCATION:

Yorktown IP Law

CONTACT #:

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In re application of: Visweswariah

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Docket No.: YOR920030401US1

For: System and Method for Incremental Statistical Analysis of Digital Circuits

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**CERTIFICATE OF FACSIMILE TRANSMISSION**

I hereby certify that this Amendment (15 pages) and Petition and Fee For Extension of Time (4 pages) is being facsimile transmitted under Rule 37 CFR 1.61(d) to the U.S. Patent and Trademark Office to 571-273-8300 on May 26, 2006

  
Doris Avendano